

IN THE CLAIMS:

The current claims follow. For claims not marked as amended in this response, any difference in the claims below and the previous state of the claims is unintentional and in the nature of a typographical error.

1. - 2. (Canceled)

3. (Currently Amended) A high frequency receiver according to claim 8, wherein (1), ~~which is provided with a front end comprising a low noise amplifier (2), and which is provided with quadrature mixers (3) coupled to the low noise amplifier (2), characterised in that the low noise amplifier is a quadrature low noise amplifier (2-1, 2-2), characterised in that quadrature paths (I, Q) of the~~ first and second quadrature low noise amplifier (2-1, 2-2) amplifiers are implemented differentially.

4. (Currently Amended) The high frequency receiver ~~[[(1)]]~~ according to claim 3, ~~characterised in that~~ wherein the differential first and second quadrature low amplifier (2-1, 2-2) is amplifiers are constructed as a class AB operating ~~circuit~~ circuits.

5. (Currently Amended) The high frequency receiver ~~[[(1)]]~~ according to claim 3, wherein ~~[[the]]~~ each of said first and second quadrature low noise amplifier (2-1, 2-2) amplifiers comprises a cascode arrangement of ~~semiconductors (15)~~; semiconductors.

6. (Currently Amended) The high frequency receiver ~~[[(1)]]~~ according to claim 5, wherein each of said first and second ~~[[the]]~~ semiconductors ~~[[(15)]]~~ are of the type MOST.

7. (Currently Amended) A high frequency receiver according to claim 5, wherein (1), which is ~~provided with a front end comprising a low noise amplifier (2), and which is provided with quadrature mixers (3) coupled to the low noise amplifier (2), characterised in that the low noise amplifier is a quadrature low noise amplifier (2-1, 2-2), in that the quadrature low noise amplifier (2-1, 2-2) comprises a cascode arrangement of semiconductors (15), and in that across the cascode arrangement of semiconductors~~ ~~[[(15)]]~~ there is connected a capacitor (C).

8. (Currently Amended) A high frequency receiver ~~[[(1)]]~~ which is provided with a front end ~~comprising~~ comprising:

a first quadrature low noise amplifier (2); amplifier; ~~[[and]]~~

a second quadrature low noise amplifier;

~~which is provided with~~ first and second quadrature mixers ~~[[(3)]]~~ coupled to respective ones of the first and second ~~[[the]]~~ low noise ~~amplifier (2)~~; amplifiers;

~~characterised in that the low noise amplifier is a quadrature low noise amplifier (2-1, 2-2),~~
~~characterised in that the high frequency receiver (1) comprises two~~ first and second quadrature
~~choppers (10-1, 10-2)~~ respectively coupled between respective outputs (4, 5) of the respective
ones of the first and second quadrature low noise amplifiers, ~~that include said amplifier and~~
~~another quadrature low noise amplifier,~~ and respective inputs of respective ones of the first and
second ~~the quadrature mixers (3-1, 3-2) whose output~~ mixers, the outputs of the first and second
mixers being ~~demodulated by a quadrature demodulator-demodulator, said choppers~~
respectively switching in-phase and quadrature signals.

9. (Currently Amended) The high frequency receiver ~~[(1)]~~ according to claim 8,
wherein the quadrature choppers (10-1, 10-2) and quadrature mixers (3-1, 3-2) are combined to
passive quadrature choppers/mixers.

10.-17. (Canceled)

18. (Currently Amended) The receiver ~~[[of]]~~ according to claim ~~[[3,]]~~8, wherein the first
and second ~~coupled~~ quadrature mixers are in a receive circuit of said receiver.

19. (Currently Amended) The receiver [[of]] according to claim 18, wherein output of said first and second mixers comprises a signal that has been down-converted by said receive circuit.

20. (Currently Amended) The receiver [[of]] according to claim 7, wherein said cascode arrangement comprises two parallel legs of said semiconductors, both legs being in parallel with said capacitor.

21. (Currently Amended) The receiver [[of]] according to claim 7, wherein said cascode arrangement comprises a differential cascode arrangement.

22. (Currently Amended) The receiver [[of]] according to claim 8, wherein each of said choppers switches its respective outputs for coupling with the outputs of the other of said choppers.

23. (Canceled)

24. (New) A high frequency receiver which is provided with a front end comprising:
a first quadrature low noise amplifier;
a second quadrature low noise amplifier;
first and second quadrature mixers coupled to respective ones of the first and second low noise amplifiers;

first and second quadrature choppers respectively coupled between respective outputs of the respective ones of the first and second quadrature low noise amplifier, and respective inputs of respective ones of the first and second quadrature mixers, the outputs of the first and second mixers being demodulated by a quadrature demodulator, wherein each of said choppers switches its respective outputs for coupling with the outputs of the other of said choppers.